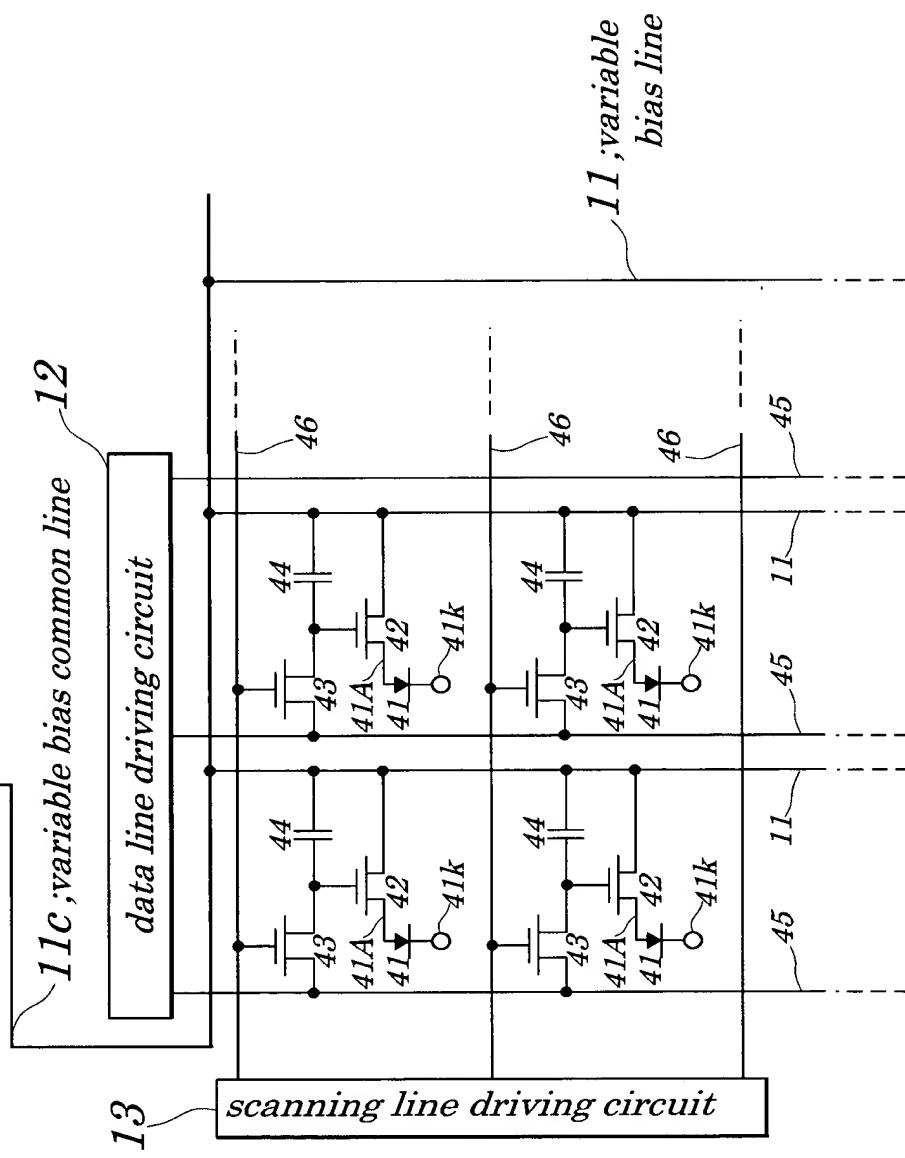
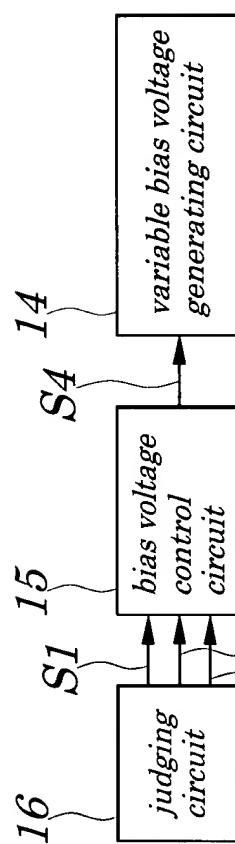


FIG.1



scanning line driving circuit

11, variable bias line

FIG.2

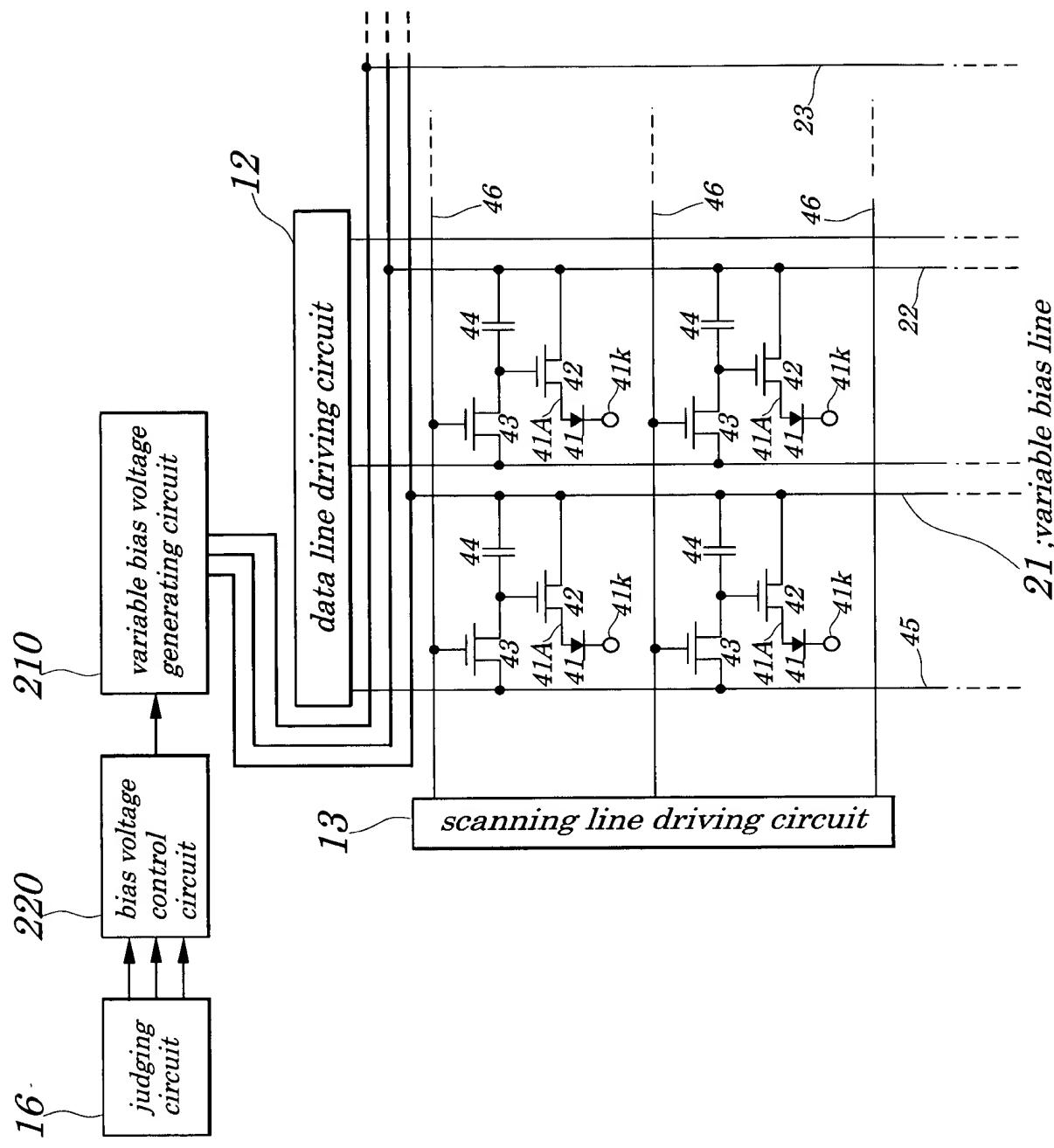


FIG.3 (PRIOR ART)

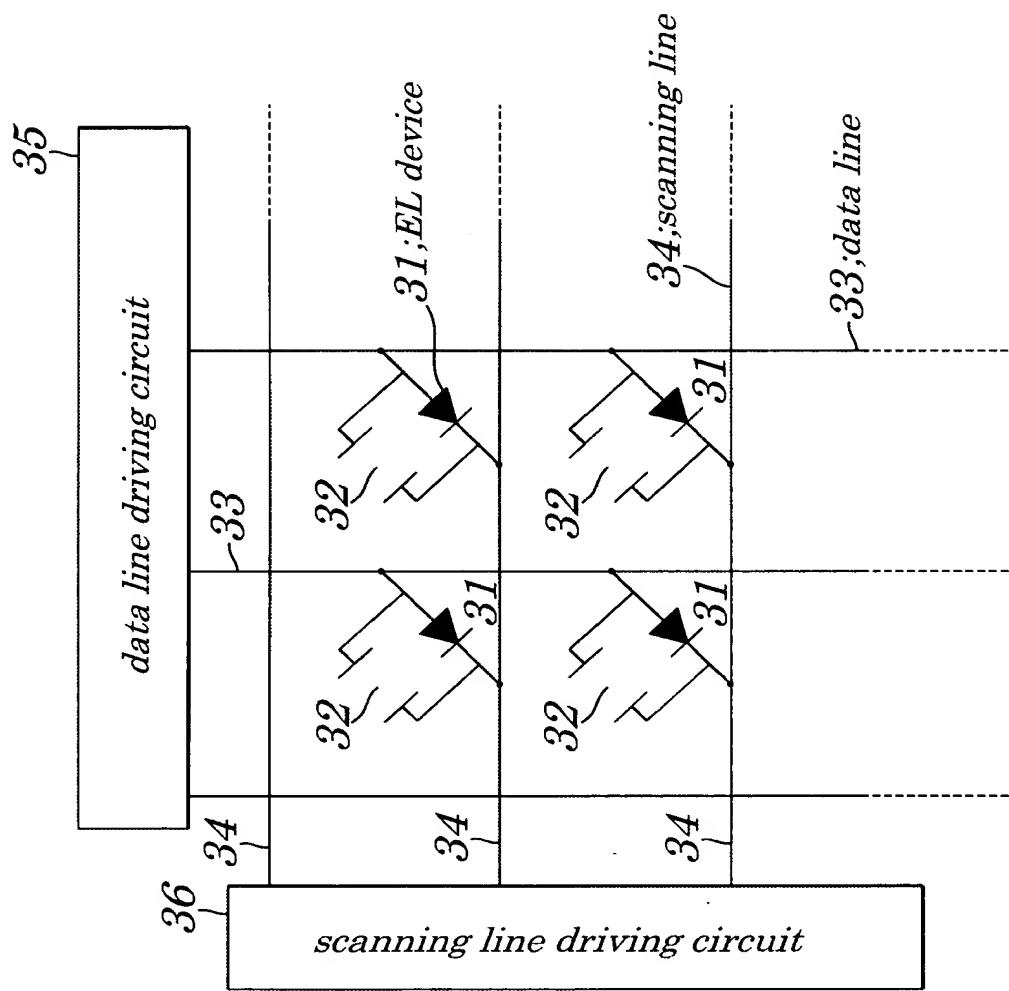


FIG. 4 (PRIOR ART)

410

**bias voltage source**

48

**data line driving circuit**

49

**scanning line driving circuit**

46; scanning line

44  
43  
41  
4246  
43  
41  
4244  
43  
41  
4244  
43  
41  
42

47; bias line

47

46

46  
43  
41  
4246  
43  
41  
4246  
43  
41  
42

45

47

45; data line

47

46

46  
43  
41  
4246  
43  
41  
42

45

47